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IBM Docket No. RPS920010127US1

**In the United States Patent and Trademark Office
Patent Application Transmittal**

Transmitted herewith for filing is the Patent Application of:

Inventors(s): Robert Thomas Bailis, Charles Edward Kuhlmann, Charles Steven Lingafelt,
Ann Marie Rincon
For: METHOD AND SYSTEM FOR USE OF A FIELD PROGRAMMABLE GATE ARRAY (FPGA)
FUNCTION WITHIN AN APPLICATION SPECIFIC INTEGRATED CIRCUIT (ASIC) TO
ENABLE CREATION OF A DEBUGGER CLIENT WITHIN THE ASIC

ASSIGNEE NAME: International Business Machines Corporation

ASSIGNEE RESIDENCE: Armonk, New York

Enclosed are

14 pages of specification, including 15 claims, plus 3 sheets of drawings.

X An assignment of the invention to International Business Machines Corporation, Armonk, New York 10504.

A certified copy of a/an application.

X Declaration and Power of Attorney.

PTO-1449 & references

X A return post card

Other:

Filing Fee Calculation (For Other Than Small Entity)

Basic Fee:						\$740.00
Claims Fees:	Filed	Limit	Extra		Rate per Extra	
Total claims:	15	20	0		\$18.00	\$0.00
Independent claims:	2	3	0		\$84.00	\$0.00
Multiple Dependent Claim Presented					\$280.00	\$0.00
					Total	\$740.00

Please charge Deposit Account **09-0464** for the **Total** set forth above. The Commissioner is authorized to charge payment of any additional filing fees required under 37 CFR §1.16 and any patent application processing fees under 37 CFR §1.17 or to credit any overpayment to the identified account. A duplicate copy of this sheet is enclosed.

Express Mail Certificate
Express Mail Label No: EL888549321US
Date: December 10, 2001
I hereby certify that I am depositing the papers identified above with the U.S. Postal Service "Express Mail Post Office to Address" service on the above date, addressed to the Assistant Commissioner for Patents, Washington, DC 20231
Karen Orzechowski
Karen Orzechowski

BY: *Joscelyn G. Cockburn*

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Date: December 10, 2001

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